

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(0123129 03267095).pn.	JPO	OR	OFF	2009/04/17 05:48
L2	1	("01231329").pn.	JPO	OR	OFF	2009/04/17 05:50
L3	0	1 and 2	JPO	OR	OFF	2009/04/17 05:52
L4	2	1 2	JPO	OR	OFF	2009/04/17 05:52
L5	0	("20010025431"   "20020166256"   "20030172542"   "20040045184"   "20040123484"   "20040216325"   "20050160619"   "20050183283"   "20060005420"   "20070169373"   "20070220775"   "20080052947"   "20080127508"   "4258731"   "4434563"   "4531307"   "4724621"   "5369891"   "5526578"   "5582721"   "5826128"   "5862605"   "6216364"   "6374512"   "6398875"   "6473993"   "6562146"   "6596093"   "6602351"   "6613157"   "6641678"   "6649883"   "6796054"   "6808567"   "6966949"   "6986214"   "7024798"   "7225819"   "7347006").PN.	JPO	OR	OFF	2009/04/17 07:22

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L7	40	("20010025431"   "20020166256"   "20030172542"   "20040045184"   "20040123484"   "20040216325"   "20050160619"   "20050183283"   "20060005420"   "20070169373"   "20070220775"   "20080052947"   "20080127508"   "4258731"   "4434563"   "4531307"   "4724621"   "5369891"   "5526578"   "5582721"   "5826128"   "5862605"   "6216364"   "6374512"   "6398875"   "6473993"   "6562146"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	OFF	2009/04/17 07:39

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L8	727250	(wafer semiconductor substrate) and temperature and (control\$3 regulat\$3 adjust\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	OFF	2009/04/17 08:17
L9	370645	(wafer semiconductor substrate) and temperature same (control\$3 regulat\$3 adjust\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	OFF	2009/04/17 08:17
L10	236297	(wafer semiconductor substrate) and temperature with (control\$3 regulat\$3 adjust\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	OFF	2009/04/17 08:17
L11	1123	(wafer semiconductor substrate) and temperature and (control\$3 regulat\$3 adjust\$3) and "34"/\$. ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	OFF	2009/04/17 08:17
L12	84	(wafer semiconductor substrate) and temperature and (control\$3 regulat\$3 adjust\$3) and "34"/78- 80.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	OFF	2009/04/17 08:18
L13	468	(wafer semiconductor substrate) and temperature and (control\$3 regulat\$3 adjust\$3) and "34"/380- 497.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	OFF	2009/04/17 08:18
L14	519	(12 13) and 8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	OFF	2009/04/17 08:19

L15	401	(12 13) and 9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	OFF	2009/04/17 08:19
L16	329	(12 13) and 10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	OFF	2009/04/17 08:19
L17	519	(12 13) and 11	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO	OR	OFF	2009/04/17 08:19

**4/ 17/ 2009 8:19:43 AM**

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